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10/786,151 02/26/2004 Mitsutoshi Miyasaka 25944 7590 12/12/2006 OLIFF & BERRIDGE, PLC	118490 EXAM	7277 INER
OLIFF & BERRIDGE, PLC	EXAM	INER
	FENTY, JESSE A	
P.O. BOX 19928 ALEXANDRIA, VA 22320	ART UNIT	PAPER NUMBER
ADEAANDRIA, VA 22520	2815	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)			
Office Action Commons	10/786,151	MIYASAKA, MITSUTOSHI			
Office Action Summary	Examiner	Art Unit			
	Jesse A. Fenty	2815			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tin 11 apply and will expire SIX (6) MONTHS from 12 cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 22 Se	entember 2006				
	action is non-final.				
·—					
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Disposition of Claims					
4)⊠ Claim(s) <u>24-32</u> is/are pending in the application					
4a) Of the above claim(s) is/are withdrawn from consideration.					
5)⊠ Claim(s) <u>25</u> is/are allowed.					
6)⊠ Claim(s) <u>24,26,28,29 and 32</u> is/are rejected.		•			
7)⊠ Claim(s) <u>27, 30, 31</u> is/are objected to.					
8) Claim(s) are subject to restriction and/or	election requirement				
	orden requirement.				
Application Papers					
9) The specification is objected to by the Examiner					
10) The drawing(s) filed on is/are: a) acce	epted or b) \square objected to by the $\mathfrak l$	Examiner.			
Applicant may not request that any objection to the o	* ' '	· ·			
Replacement drawing sheet(s) including the correcti					
11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.			
Priority under 35 U.S.C. § 119					
 12) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority documents 		-(d) or (f).			
2. Certified copies of the priority documents	have been received in Applicati	on No			
3. Copies of the certified copies of the prior	ity documents have been receive	ed in this National Stage			
application from the International Bureau	(PCT Rule 17.2(a)).				
* See the attached detailed Office action for a list of	of the certified copies not receive	d.			
Attachment(s)	,	(272)			
Notice of References Cited (PTO-892)	4) Ll Interview Summary Paper No(s)/Mail Da				
3) Information Disclosure Statement(s) (PTO/SB/08)	5) 🔲 Notice of Informal P				
Paper No(s)/Mail Date	6) Other:				

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 24, 26, 28, 29 and 32 are rejected under 35 U.S.C. 102(e) as being anticipated by Yamazaki et al. (US 2003/0132483 A1).

In re claim 24, Yamazaki (e.g., Fig. 1) discloses a complementary thin film transistor circuit, comprising:

a substrate;

a first thin film transistor that is formed on the substrate, the first thin film transistor including a first semiconductor film, a first gate electrode, and a first gate insulating film disposed between the first semiconductor film and the first gate electrode, and the first semiconductor film including a first source region (115), a first drain region (119), and a first channel region (117); and

a second thin film transistor that is formed on the substrate, the second thin film transistor including a second semiconductor film, a second gate electrode, a second gate insulating film disposed between the second semiconductor film and the second

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gate electrode, the second semiconductor film including a second source region (126), a second drain region (122) and a second channel region, the first drain region and the second drain region being in contact with each other and the first source region being separated from the second source region.

The same configuration is also disclosed by Yamazaki et al. (US 2002/0117736 A1) and Yamazaki et al. (U.S. Patent No. 6,617,644 B1). Both of said references are also cited here to reject claim 24 under 35 USC 102(e).

In re claim 26, Yamazaki et al. discloses the device of claim 24, further comprising

a plurality of crystal grains formed on the substrate, each of the plurality of crystal grains being a single crystal grain, one of the plurality of crystal grains including the first semiconductor film and the second semiconductor film (section [0080]).

In re claim 28, Yamazaki et al. discloses the device of claim 24, wherein the first channel region and the second channel region have a same plane orientation.

In re claim 29, Yamazaki et al. discloses the device of claim 24, wherein the first TFT is N-channel and the second TFT is P-channel.

In re claim 32, Yamazaki et al. discloses the device of claim 24, wherein the TFT is a complementary TFT transistor circuit.

Allowable Subject Matter

Claim 25 is allowed.

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Claims 27, 30 and 31 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

Applicant's arguments with respect to claims 1 – 23 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jesse A. Fenty whose telephone number is 571-272-1729. The examiner can normally be reached on M-F 5/4.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ken Parker can be reached on 571-272-2298. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JAF

KENNETH PARKER SUPERVISORY PATENT EXAMINER